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ERY CONTR	OL FORM				RT/S US	SE ONLY	
Application No.	09/50	1,033	Prepared by	NAB	Tracking Number	05900374	
Examiner-GAU	NGUVE	71-2829	Date	417/04	Week Date	2/2/04	
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a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449		
b. Applicant(s)	g. Disclaimer	I. Print Fig.	q. PTOL-85b		
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract		
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs		
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other		

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SPECIFICATION	MESSAGE
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b. Text Continuity	claim 57 (now claim 56) ends incomplete.
c. Holes through Data	(See attached)
d. Other Missing Text	
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i. Appendix	
j. Amendments	
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57. (Four Times Amended) A system for electrically coupling a semiconductive device to an electrical apparatus, the system comprising:

an interposer, the interposer comprising:

a substantially homogeneous, substantially planar sheet having an outermost surface and composed of an electrically insulating material selected from the group consisting of oxides of silicon, silicate glass, and nucleated, substantially crystalline glass, and mixtures and derivatives thereof, said sheet being configured for receiving thereon a semiconductive device such that said semiconductive device lies at least in part on said outermost surface and is unimbedded into said substrate; and

an electrical conductor on the sheet, the electrical conductor having a receiving end for connecting to the semiconductive device at electrically conductive terminals of said semiconductive device and a terminal end for connecting to the electrical apparatus, such that the semiconductive device is electrically coupled to the electrical apparatus when the semiconductive device is connected to the receiving end of the electrical conductor and the terminal end of the electrical conductor is connected to the electrical apparatus, wherein at least some of said terminals are located in the region between said semiconductive device and said outermost surface of said substrate; and

a connector for holding the semiconductive device stationary relative to the interposer, wherein said connector is in contact engagement with said semiconductive device and with said sheet, wherein said contact engagement is effectuated through direct contact between the connector with the semiconductor device or through an adhesive in contact with said connector.